BAPApplicant: Haruo Hyodo et al.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:

09/963,267.

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Filed Title

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Art Unit

: 2822

Examiner: Ida M. Soward

Commissioner for Patents Washington, D.C. 20231

COMMUNICATION TO FULFILL THE DUTY TO INFORM

THE EXAMINER ON MATERIAL CO-PENDING US APPLICATIONS

In response to a duty to disclose information material to patentability under CFR §1.56. Applicants comment as follows:

In request to fulfill the duty to inform the Examiner on any co-pending US applications material to the present application, Applicants bring to the attention of the Examiner, Application Serial No. 09/963,839 (Attorney Docket No.: 10417-100001).

Respectfully submitted,

12/06/02

Fish & Richardson P.C. 45 Rockefeller Plaza, Suite 2800 New York, New York 10111 Telephone: (212) 765-5070

Facsimile: (212) 258-2291

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Chris T. Mizumoto Reg. No. 42,899.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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